



PATENT

#1512
NANO-001/05US
N0765-2008

I hereby certify that this correspondence is being deposited with the United States Postal Service with sufficient postage as first class mail in an envelope addressed to the Assistant Commissioner for Patents, Washington, D.C. 20231, on February 6, 1998.

Date: 2/6/98

By: Patricia K. Parry
Patricia K. Parry

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re application of)

Charles H. Moore et al.)

Serial No. 08/484,918)

Filed: June 7, 1995)

For: HIGH PERFORMANCE)
MICROPROCESSOR HAVING)
VARIABLE SPEED)
SYSTEM CLOCK)

Examiner: D. Eng

Art Unit: 2784

AMENDMENT

Palo Alto, CA 94306

Assistant Commissioner for Patents
Washington, D.C. 20231

Sir:

This Amendment is being submitted in response to the Office Action dated October 16, 1997 in the above-identified patent application.

IN THE CLAIMS

Please amend claims 19, 65, 73 and 78 as follows:

Sub 71
19(Three Times Amended). A microprocessor system, comprising a single integrated circuit including a central processing unit and [a] an entire ring oscillator variable speed system clock in said single integrated circuit and connected to said central processing unit for clocking said central processing unit, said central processing unit and said ring oscillator variable speed system clock each including a plurality of electronic devices correspondingly constructed of the same process technology with corresponding manufacturing variations, a processing frequency capability of said central processing unit and a speed of said ring oscillator variable speed system clock varying together due to said manufacturing variations and due to at least operating voltage and temperature of said single integrated circuit.